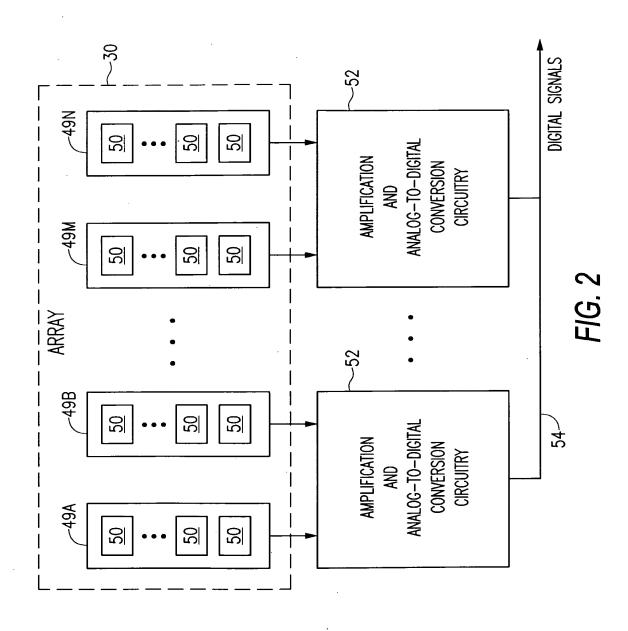


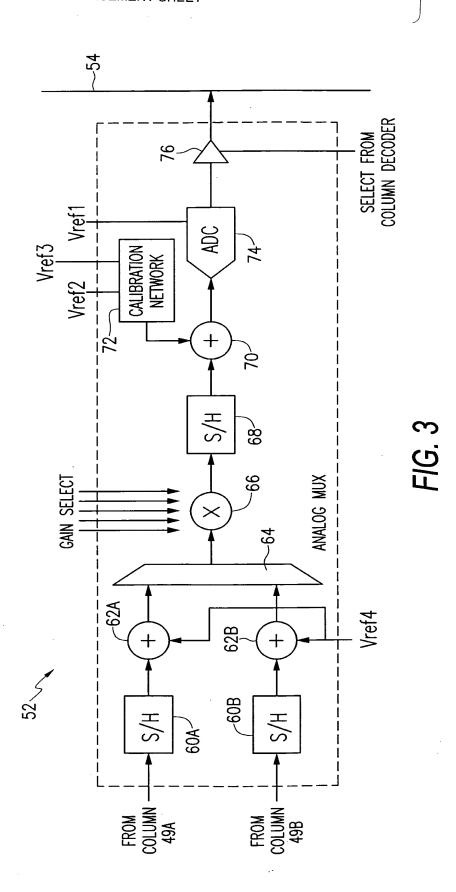
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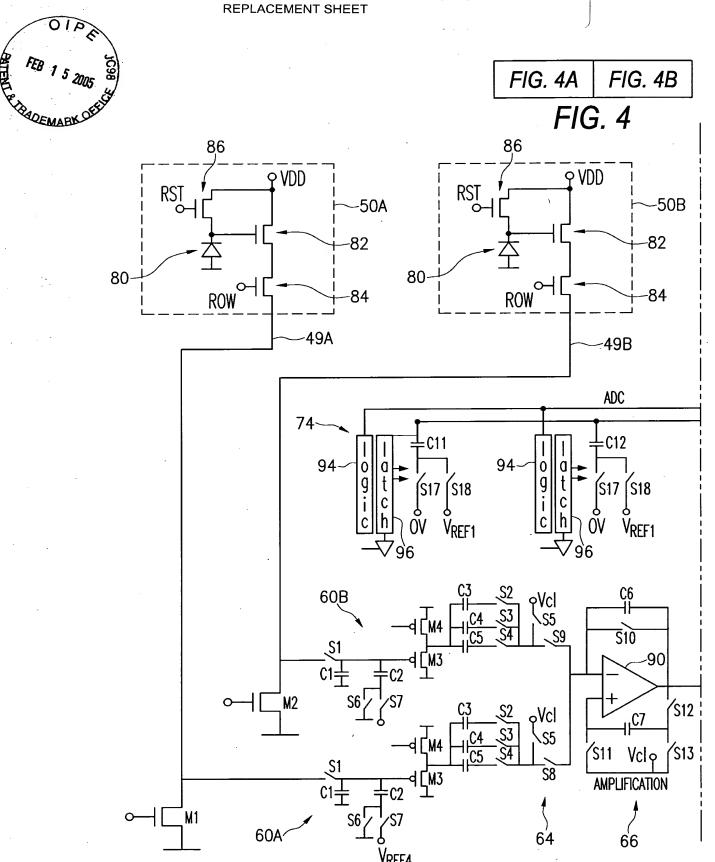


FIG. 4A

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FIG. 4A FIG. 4B

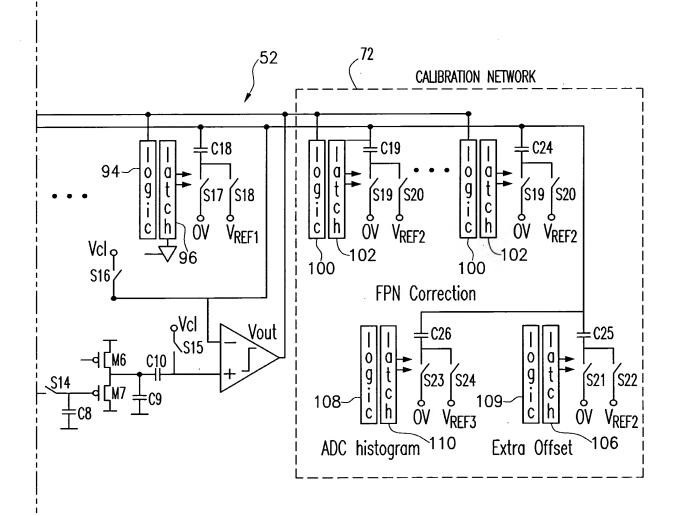
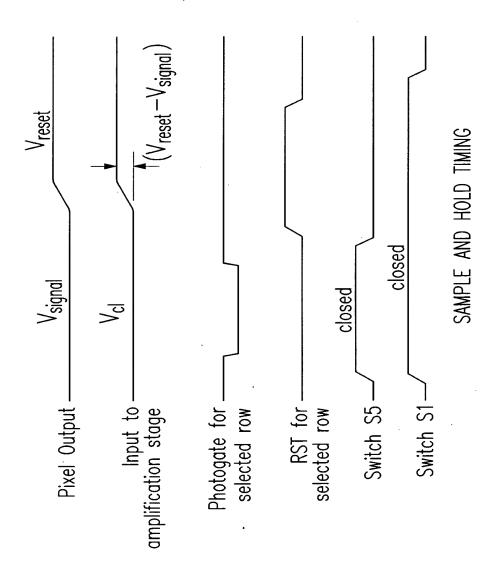
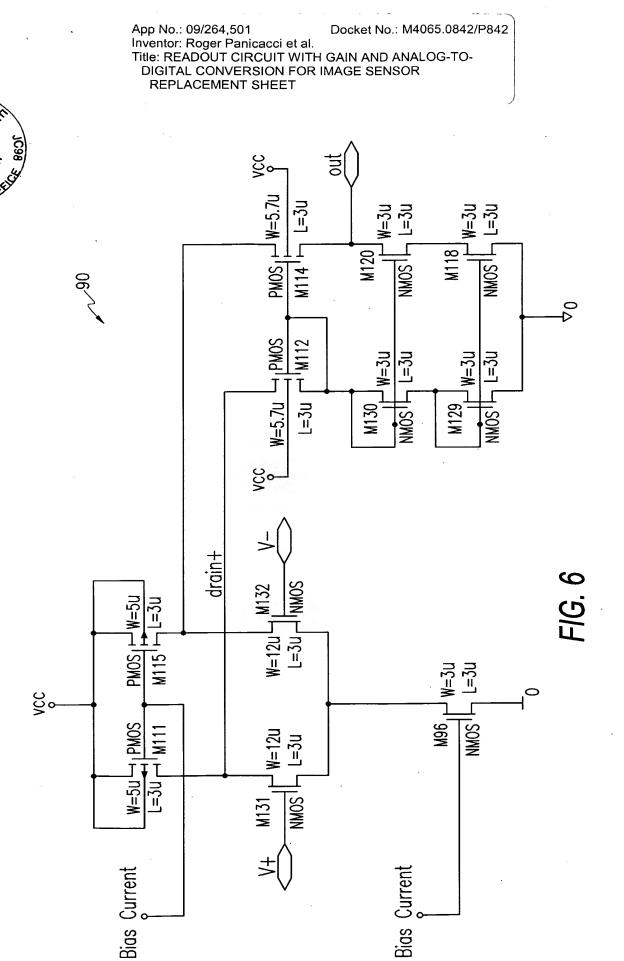


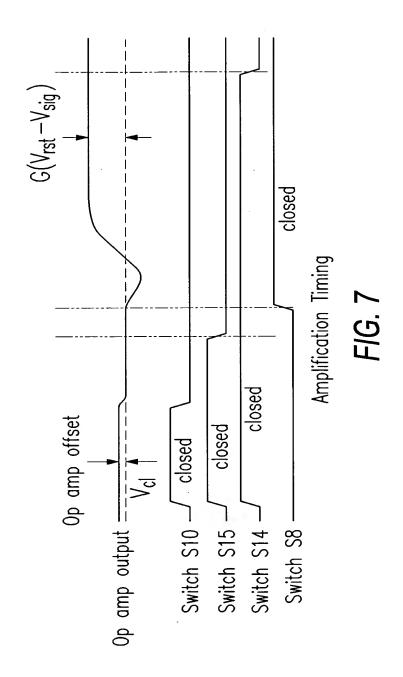
FIG. 4B









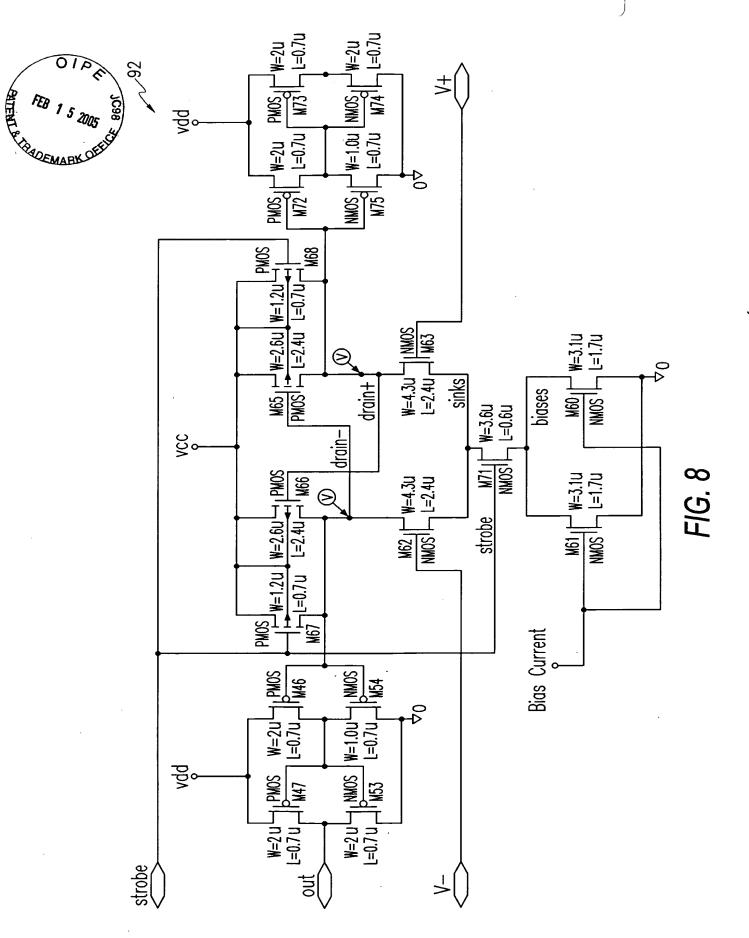


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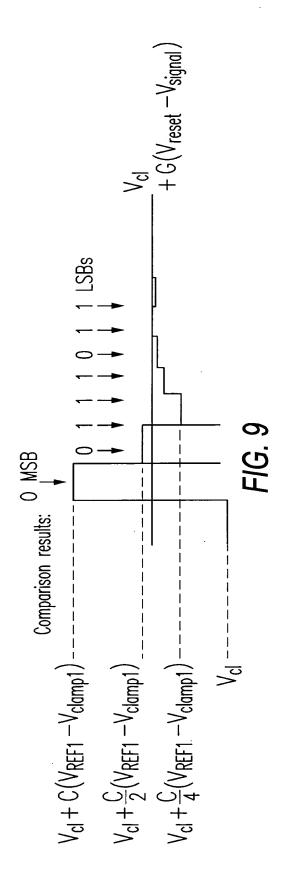
Docket No.: M4065.0842/P842

Inventor: Roger Panicacci et al.

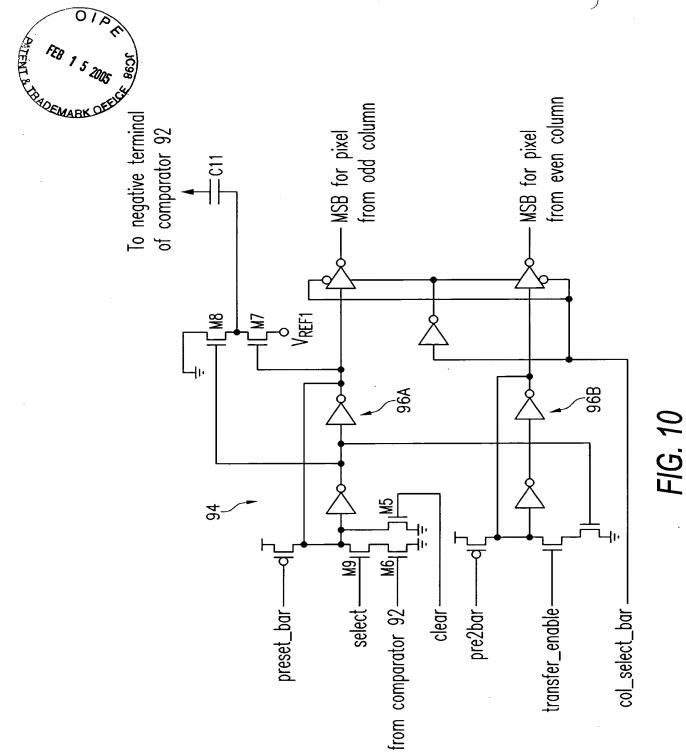
Title: READOUT CIRCUIT WITH GAIN AND ANALOG-TO-

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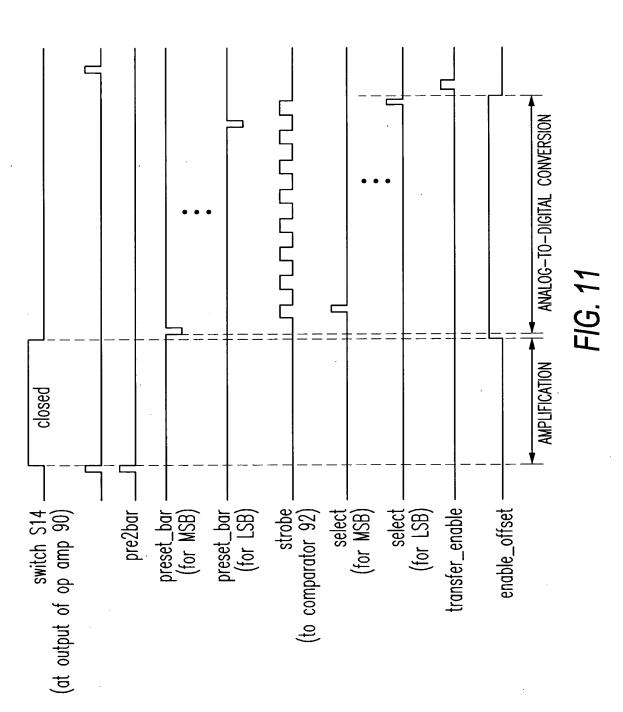


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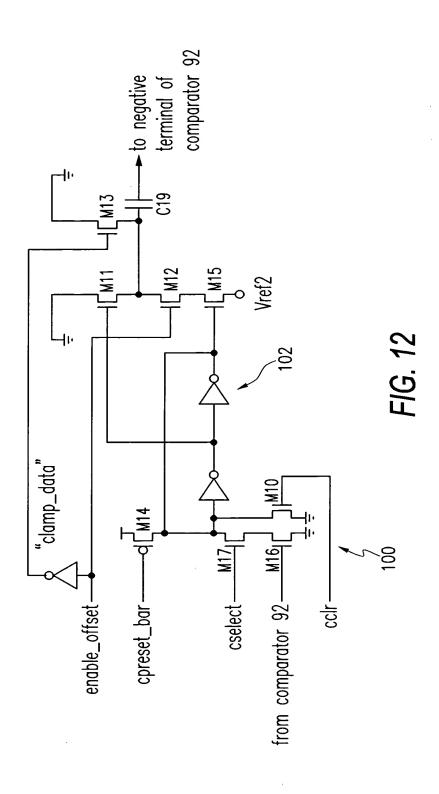


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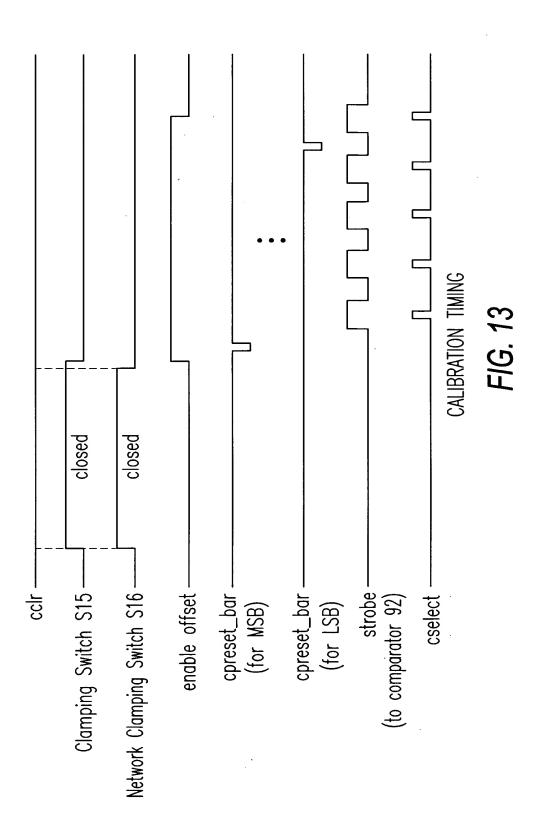












Title: READOUT CIRCUIT WITH GAIN AND ANALOG-TO-DIGITAL CONVERSION FOR IMAGE SENSOR REPLACEMENT SHEET FIG. 14A FIG. 14B FIG. 14 Sample and Hold 50A QVDD (even channel) 184/.57 92 60A phi_s0 shs RST. 1.15/.5 86 M32 M30 C30 1490 84 phi_r0 shr RÓW Vaa - M33 vlp1d 3/0.5 3/0.5 C31 (~2.8V 1490 _3.0/1.3 $vln1d (\sim 1.1V) - \frac{1}{M2} = 3.8/1.3$ C33 75= d [6.6/1.3 151= S30 S32 **ี**3/0.5 ั M35 -node_reset 1.2/0.5 62A vref4 phi_s1 phi_r1 from. pixel 50B 60B odd channel <u>62B</u>

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FIG. 14A

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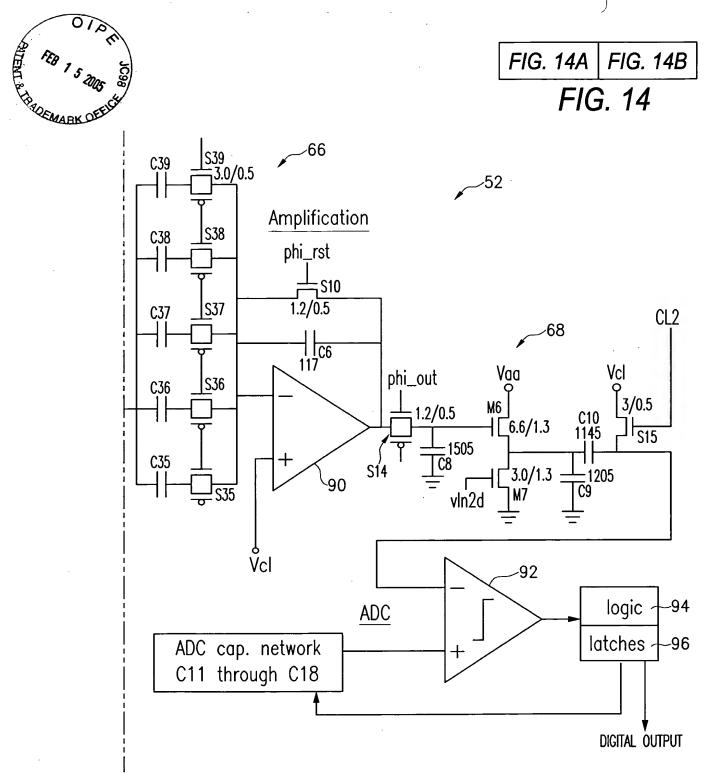


FIG. 14B

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